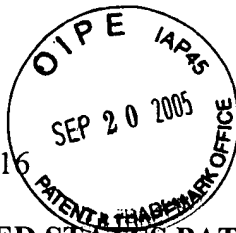


Docket No.: 067471-0016



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
TAKAYAMA, SHUICHI, et al. : Confirmation Number: 2973
Application No.: 10/045,802 : Group Art Unit: 2191
Filed: October 19, 2001 : Allowed: July 7, 2005
Examiner: CHAVIS, JOHN Q

For: PROCESSOR FOR EXECUTING INSTRUCTIONS IN UNITS THAT ARE
UNRELATED TO THE UNITS IN WHICH INSTRUCTIONS ARE READ, AND
A COMPILER, AN OPTIMIZATION APPARATUS, AN ASSEMBLER, A
LINKER, A DEBUGGER AND A DISASSEMBLER FOR SUCH PROCESSOR

LETTER SUBMITTING FORMAL DRAWINGS
AS REPLACEMENT SHEETS

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability dated July 7, 2005, submitted herewith
is One (1) sheet of Formal Drawings as a Replacement Sheet in connection with the
above referenced application.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Michael E. Fogarty
Registration No. 36,139

**Please recognize our Customer No. 20277
as our correspondence address.**

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Washington, DC 20005-3096
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Facsimile: 202.756.8087
Date: September 20, 2005